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					MIPS.0107-00-US

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		6505290		01-07-2003	Moyer et al	
		5082041		10-29-1991	William S. Zuk	
		4897779		01-30-1990	Dickson et al	
		4694768		01-16-1990	Iwasaki et al	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
WMT	AA	JONES, DARREN. "Opal Coprocessor Interface User's Manual." 08/20/99, MIPS Technologies, Inc., Mountain View, CA, US	
WMT	AB	BRUB, ROLF-JURGEN. RISC, The MIPS-R3000 Family. Berlin and Munich: Siemens Aktiengesellschaft., 1991. pp51-65	
WMT	AC	RIORDAN, TOM. MIPS R3010 Floating-point Coprocessor Interface. MIPS Computer Systems, Inc. October 27, 1988.	
WMT	AD	Motorola, Inc. "MC68020 and MC68EC020 Microprocessors User's Manual, 1st Edition," 1992, pp.7-10 to 7-14	
WMT	AE	LR3010 and LR3010A, MIPS Floating-Point Accelerator User's Manual. LSI Logic Corporation. 1991.	
WMT	AF	LR3000 and LR3000A, MIPS RISC Microprocessor User's Manual. LSI Logic Corporation. 1991. pp.i to 1-18 and 8-1 to 8-10.	
WMT	AG	R3000A Processor Interface. Preliminary. March 9, 1990.	

Examiner Signature	W. TREAT	Date Considered	1/26/06
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